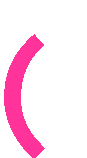
**on**

**A**

**P**

**roject**

**Abstract**

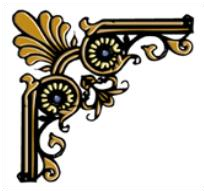
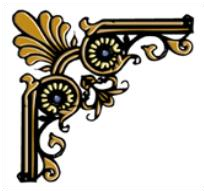


**INDIA**

**2016**

**-**

**2020**



**POWER SYSTEM FAULT DIOGNOSIS USING NEURAL NETWORKS**

# *submitted in partial fulfilment of requirements for the award of the degree of*

BACHELOR OF TECHNOLOGY

*in*

# ELECTRICAL AND ELECTRONICS ENGINEERING

*by*

**EDAGOTTI PAVANKUMAR 16121A0253**

**AVILALA DEDEEPYA 16121A0208**

**GAYAKWADA CHANDRASEGHAR 16121A0265**

**AVULA PRAVALIKA 16121A0209**

*under the guidance of*

# **Dr. S. HEMACHANDRA *M.Tech., Ph.D***

# **Professor**



**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

## A. RANGAMPET, TIRUPATI – 517 102